CHAPTER V

# INPUT AND OUTPUT

*The standard input and output units are treated in separate maker's handbooks, which are supplied with the machine; the purpose of this chapter is to describe how the units are used with the Pegasus computer. The logical circuits controlling input and output are described in Chapter XII of Volume 2.*

Ferr. Mk V

## INPUT

1. The standard Pegasus computer is designed for use with two Ferranti high-speed photoelectric tape readers, Mk II, which are associated with external conditions 0 (reader 1) and 1 (reader 2). Each reader carries two 12-pin plugs, A and B, which are cable-connected to sockets on the desk, and thence to sockets on bay 1 of the cabinet, plugs A and B, being connected to sockets SK9 and SK10 (for reader 1) or SK17 and SK18 (for reader 2) respectively. There is a direct correspondence between the pins on plugs A and B, and the pins on the computer socket board, signals on the pins being as follows:

| Pin | Pl. A SK9 or 17 | Pl. B SK10 or 18 |
|---|---|---|
| A | phase | sprocket hole |
| B | ] | l.s. digit |
| C | ] | digit 4 |
| D | neutral | digit 3 |
| E | | digit 2 |
| F | brake drive | m.s. digit |
| G | +300V to brake | |
| H | | |
| J | +300V to clutch | E |
| K | clutch drive | -300V |
| L | E | -150V |
| M | +300V (anodes) | ~~+150V~~ +260 |