connected so as to reduce the distortion caused by stray capacitance at its cathode; in particular, any tendency for V1 to cut off when its grid is driven rapidly in a negative direction is reduced owing to the extra current that is available for discharging the cathode capacitance. The value of resistor R4 is chosen so that the output is at -10V when the input is at -10V. The circuit is built, as shown in the figure, on five separate panels, which are supported within the cylindrical body of the unit.